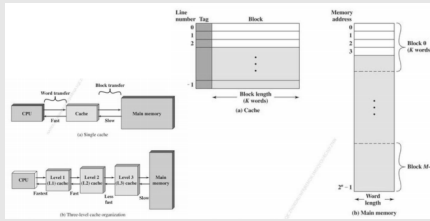


Memory System Overview

| | |
|--|---|
| Location Internal (e.g., processor registers, cache, main memory) External (e.g., optical disks, magnetic disks, tapes) | Performance Access time Cycle time Transfer rate |
| Capacity Number of words Number of bytes | Physical Type Semiconductor Magnetic Optical Magneto-optical |
| Unit of Transfer Word Block | Physical Characteristics Volatile/nonvolatile Erasable/nonerasable |
| Access Method Sequential Direct Random Associative | Organization Memory modules |

Main Memory and Cache



Cache Design

| | |
|---|--|
| Cache Addresses Logical Physical | Write Policy Write through Write back |
| Cache Size | Line Size |
| Mapping Function Direct Associative Set associative | Number of Caches Single or two level Unified or split |
| Replacement Algorithm Least recently used (LRU) First in first out (FIFO) Least frequently used (LFU) Random | |



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Not published yet.
 Last updated 12th December, 2017.
 Page 1 of 1.

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