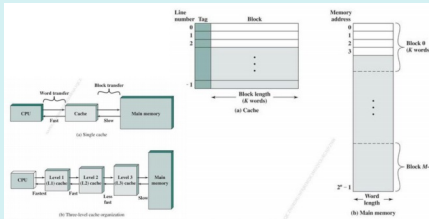


### Memory System Overview

<b>Location</b>	Internal (e.g., processor registers, cache, main memory) External (e.g., optical disks, magnetic disks, tapes)	<b>Performance</b>	Access time Cycle time Transfer rate
<b>Capacity</b>	Number of words Number of bytes	<b>Physical Type</b>	Semiconductor Magnetic Optical Magneto-optical
<b>Unit of Transfer</b>	Word Block	<b>Physical Characteristics</b>	Volatile/nonvolatile Erasable/nonerasable
<b>Access Method</b>	Sequential Direct Random Associative	<b>Organization</b>	Memory modules

### Main Memory and Cache



### Cache Design

<b>Cache Addresses</b>	Logical Physical	<b>Write Policy</b>	Write through Write back
<b>Cache Size</b>		<b>Line Size</b>	
<b>Mapping Function</b>	Direct Associative Set associative	<b>Number of Caches</b>	Single or two level Unified or split
<b>Replacement Algorithm</b>	Least recently used (LRU) First in first out (FIFO) Least frequently used (LFU) Random		



By samcattanach

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