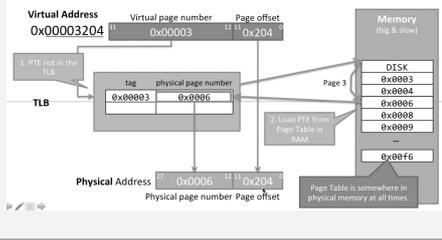


Translation lookaside buffer (TLB) Example:

Example translation (empty TLB)



Cache Organization

Parameter	Description
2^m	Number of cache lines
E	Number of lines per set
2^k	Bytes per cache line
2^r	Number of cache lines per memory address bit
2^s	Number of cache lines per memory address bit
2^t	Minimum number of cache memory addresses
2^u	Number of set index bits
2^v	Number of direct address bits
2^w	Number of tag bits
$2^x + 2^y + 2^z$	Cache size (bytes) including overhead in the cache and tag bits

Organization of Cache (S,E,B,m)

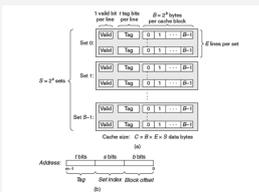


Figure 6.25 General organization of cache (S, E, B, m).



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