Branches (Jumps)

B	Branch
BL	Branch with Link (like x86 CALL)
BNE	Branch if not equal

Comparators

CMP	Sub style comparison
TEST	And style comparison

Arithmetics

ADD	Addition
SUB	Substract
RSB	Reverse substract

Register assign

MOV	Move a value to a register
MVN	Performs a bitwise NOT before move
NEG	Multiply by -1 before move

Register Operations

LDR	Load register
STR	Store register (word)
STRB	Store least-significant byte
STRH	Store lower halfword contained