# Cheatography

## CSCI Final Cheat Sheet by JMR via cheatography.com/36949/cs/11642/

### **Machine Cycle/Processing Cycle**

Fetch - Retrieves the next program instruction from memory.

Decode - Determines what the program is telling the compy to do.

Execute - Performs the requested instruction.

Store - Stores the results to an internal register or to memory.

#### **Basic HTML Structure**

<!DOCTYPE html>

<html>

<head>

<title>website

title</title>

</head>

<body>

content of website ...

</body>

</html>

#### Random Bytes

\*Bit = Smallest piece of data a compy works with

\*8 Bits = 1 byte aka a single unit of storage

\*Binary Data = written in 1s and 0s

### **System Unit Main Components**

Motherboard	Internal Speaker
CPU	Drive Bays
Power Supply	Expansion Slots
Cooling Fan	

# Abbreviations

CSS - HTML - Hyper Text
Cascading Markup Language
Style Sheet

CPU - VoIP - Voice over Central Internet Protocol Processing

Unit LAN - Local

Access

Network

WAN - Wide Area Network

POST - CMOS - Comp.
Power-on Metal Oxide
Self-Test Semiconductor

BIOS - Basic input/Output Sys.

## Access 2013

4 Objects = Rows = Tables, Forms Records,
Queries & Columns = Reports Fields

### JavaScript/HTML Thing

<form></form> <input> </input> =

within form to alert -

"document.write" to print

simple popup window w/

ok button

onClick = event that's triggered

<script>

when a user clicks something

Conditional if Statements (if/else) (something

happens)
{takes
action}

Call Function by using its name & listing constants

else{takes a different

6 Steps of Booting a System

1.) BIOS (Basic Input/Output System) is load

2.) Power-on self test (POST) is completed

3.) OS is loaded

4.) System Configuration is accomplished

5.) System utilities are loaded

6.) User is authenticated

### **Memory Bank**

RAM (Random Access Memory)
\*Volatile

\*Temporarily stores data & instructions for CPU Cache Memory

\*Small unit of ultrafast memory built into or near the processor \*Stores frequently or recently

access info \*3 Lvls. = L1 (primary), L2

(secondary), L3 ROM (Read-only Memory)

\*Nonvolatile

\*BIOS - 1st code when compy is powered on

\*CMOS - Starts the power-on self-test

POST - checks circuitry & RAM Bootstrap Loader - locates & loads OS into RAM

| <h_> </h_>   | Heading (h1-h6)                 |
|--|---------------------------------|
| <b></b>  | bold                            |
| <a<br>href="url"&gt;</a<br>  | Create a link                   |
| <img<br>src="filename.jp<br/>g"&gt;</img<br>   | Show an Image                   |
| <ul> <li><li>list</li> <li></li></li></ul>   | Unordered,<br>bullet-point list |
| <ol> <li>list</li> <li><ol> </ol> <li><ol> <li><ol> <li><ol> <li><ol> <li><ol> <li><ol> </ol> <li><ol> <li><ol> <li><ol> <li><ol> <li><ol> </ol> <li><ol> <li><ol> <li><ol> <li><ol> <li><ol> </ol> <li><ol> </ol></li></li></ol></li></ol></li></ol></li></ol></li></li></ol></li></ol></li></ol></li></ol></li></li></ol></li></ol></li></ol></li></ol></li></ol></li></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol></li></ol> | Ordered List                    |

Define a Table

Table Row within

table

header cell

table row

within table row

HTML Basics

### Table cell within

- 1.) External Style Sheet link to style sheet in the HTML file.
- 2.) Internal Style Sheet put in the HTML file; put the style section in the header section.
- 3.) Inline Styles Add the style formatting in the desired line by "style=".



action}