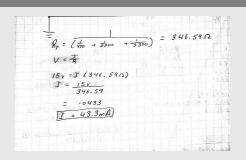


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- * Current flow is even throughout
- * Total resistance (Rt) is equal to the sum of all resistors
- * The sum of voltage drops (Vr1+Vr2+Vr3) is equal to the applied voltage (Vt) (Kristoff's Law)

Parrallel Circuit



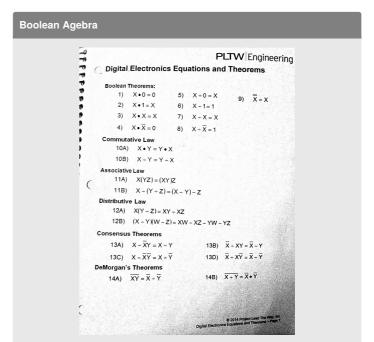
- * Voltage stays the same throughout
- * The Rt is equal to teh reciprical of the sum of the reciprical.
- * (1/Rt=1/R1+1/R2+1/R) or (Rt=1/1/R1+1/R2+1/R3)

Binary

28	27	126	25	24	23	22	2'	20
256	128	64	32	16	8	4	2	1.

AOI

AND- 74LS08 OR-74LS32 Inverter-74LS04



SI Prefix

Valve	Prefix	Symbol
1012	tera	T
109	giga	61
106	mega	M
103	Kilo	K
10-3	mili	m
10-6	micro	M
10-9	nano	n
10-12	pico	P
10-15	fermto	f



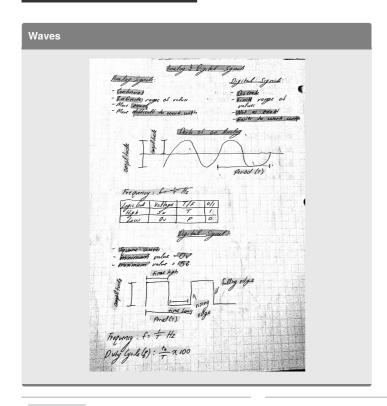
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